

AS4C256M8D3LC-12BCN vs MT41J256M8DA-125:K Comparison

Part Number & result Parameter	AS4C256M8D3LC-12BCN	MT41J256M8DA-125:K	Comparison Result
Product Description	DDR3L SDRAM	DDR3 SDRAM	DDR3L latest
Capacity	2Gb (256M x 8)	2Gb (256M x 8)	Same
Memory Organization	32Mwords, x8bits, x8 banks	32Mwords, x8bits, x8 banks	Same
Operating Power Supply	$V_{DD} \& V_{DDQ} = 1.35V$ (1.283V to 1.45V) Backward Compatible to 1.5 (+/-0.075V)	$V_{DD} \& V_{DDQ} = 1.5V$ (+/- 0.075V)	Alliance part better
Operating Temperature	Commercial (0°C to 95°C)	Commercial (0°C to 95°C)	Same
Clock Frequency	800MHz	800MHz	Same
Data Rate (MT/s)	1600	1600	Same
CAS Latency	11	11	Same
tRCD & tRP (ns)	13.75	13.75	Same
Average Refresh Period	8192 cycles/64ms upto 85C	8192 cycles/64ms upto 85C	Same
I/O Capacitance	Comparable		Same
Pin to Pin Compatible	Pin to Pin Compatible		Same
AC/DC Characteristics	Same	Same	Meet JEDEC
IDD Specification			
IDD Spec conditions	-40C to 95C	0C to 85C	
I_{DD0} (mA)	70	42	
I_{DD1} (mA)	80	56	
I_{DD2P0} (mA)	15	12	
I_{DD2P1} (mA)	22	15	
I_{DD2N} (mA)	35	34	
I_{DD2Q} (mA)	35	22	
I_{DD3P} (mA)	35	22	
I_{DD3N} (mA)	55	35	
I_{DD4R} (mA)	155	100	
I_{DD4W} (mA)	160	103	
I_{DD5B} (mA)	145	182	
I_{DD6} (mA)	15	12	
I_{DD6ET} (mA)	20	15	
I_{DD7} (mA)	240	163	
I_{DD8} (mA)	14	14	
Package (78b FBGA)	(7.5mm x 10.5mm x 1.0mm)	(8mm x 10.5mm x 1.2mm)	
Package Material	Pb and Halogen Free	Pb and Halogen Free	