



Alliance Memory Inc.
12815 NE 124th St., Suite D, Kirkland, WA 98034 USA
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Product Discontinue Notification (PDN)

Date: September 30th 2025

PDN TRACKING NO: PDN#30092025

Subject: Product Discontinue Notification (PDN) for Alliance Memory **4Gb (256Mx16 & 512Mx8) DDR4 SDRAM Rev.A** products in 96ball and 78ball FBGA Packages

Affected Part Numbers	AS4C256M16D4A-62BCN/TR AS4C256M16D4A-62BIN/TR AS4C256M16D4A-75BCN/TR AS4C256M16D4A-75BIN/TR AS4C512M8D4A-75BCN/TR AS4C512M8D4A-75BIN/TR
Description of Change:	Product Discontinuation Notice for all affected part Rev.A 4Gb DDR4 SDRAM Parts (x16 & x8)
Reason for Change	Portfolio optimization and rationalization due to resource allocations
Traceability, Guidelines (lot, date code, markings, shipment date)	Traceable through marketing orderable part numbers
Alternative Part	Refer Table.1
Summary of Alternative	Refer Table.1

Table.1

SN	Affected Part Numbers	Alternative Parts (Active in Mass Production)	Alternative New Revision Part Numbers (coming soon in Q1'2026)
1	AS4C256M16D4A-62BCN/TR	AS4C256M16D4-75BCN/TR	AS4C256M16D4B-62BCN/TR
2	AS4C256M16D4A-62BIN/TR	AS4C256M16D4-75BIN/TR	AS4C256M16D4B-62BIN/TR
3	AS4C256M16D4A-75BCN/TR	AS4C256M16D4-75BCN/TR	AS4C256M16D4B-62BCN/TR
4	AS4C256M16D4A-75BIN/TR	AS4C256M16D4-75BIN/TR	AS4C256M16D4B-62BIN/TR
5	AS4C512M8D4A-75BCN/TR	AS4C512M8D4-75BCN/TR	AS4C512M8D4B-62BCN/TR
6	AS4C512M8D4A-75BIN/TR	AS4C512M8D4-75BIN/TR	AS4C512M8D4B-62BIN/TR

Last Time Buy Date:	December 30 th , 2025
Last Time Ship Date:	June 30 th , 2026
Sample Availability Date for alternative part number	Alternative parts are already in production
PCN Effective Date	September 30 th , 2025

Please contact your local sales representative if you have any questions regarding this PDN.



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Dear Valued Customer:

This letter provides Product Discontinue notification for 4Gb (256Mx16 & 512M x8) DDR4 SDRAM product revision "A" part numbers AS4C256M16D4A-62BCN/TR, AS4C256M16D4A-62BIN/TR, AS4C256M16D4A-75BCN/TR, AS4C256M16D4A-75BIN/TR, AS4C512M8D4A-75BCN/TR and AS4C512M8D4A-75BIN/TR as part of Supply strategy to optimize the product portfolio due to resource allocations

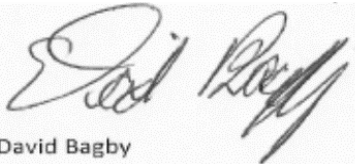
Please refer to the Table.1 for alternative active part numbers currently in Mass Production with immediate supply for customers to migrate. Additional new revision **Rev.B** part numbers are targeted to launch to the market in Q1'2026 for strong and secure supply chain of 4Gb DDR4 from Alliance Memory Inc.

The delivery deadline or last time ship date is **June 30th, 2026** with effective immediate last time buy (LTB) deadline on **December 30th, 2025**. We urge customers to place Non-Cancellable, Non-Reschedulable ("NCNR") Last-Time Buy Orders of the affected product(s) by **December 30th, 2025**.

Alliance Memory Inc. will make all reasonable commercial efforts to honor all purchase orders placed before LTB and scheduled delivery dates but reserves the right to not accept new orders or to cancel existing orders if they cannot be fulfilled by the Fab.

Please contact your local Alliance Memory representative if you have any questions regarding this product discontinue notification.

Yours faithfully


David Bagby
President
Alliance Memory Inc.





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AS4C256M16D4A-62BCN /AS4C256M16D4A-75BCN vs AS4C256M16D4-75BCN Comparison

Part Number & result Parameter	AS4C256M16D4A-62BCN	AS4C256M16D4A-75BCN	AS4C256M16D4-75BCN
Product Description	DDR4 SDRAM, Rev.A	DDR4 SDRAM, Rev.A	DDR4 SDRAM Rev.0
Capacity	4Gb (256M x 16)	4Gb (256M x 16)	4Gb (256M x 16)
Memory Organization	32Meg, x16bits, x4 banks x2 groups	32Meg, x16bits, x4 banks x2 groups	32Meg, x16bits, x4 banks x2 groups
Operating Power Supply	V _{DD} & V _{DDQ} = 1.2V (+/-0.06V) V _{pp} = +2.5V(-0.125V/+0.25V)	V _{DD} & V _{DDQ} = 1.2V (+/-0.06V) V _{pp} = +2.5V(-0.125V/+0.25V)	V _{DD} & V _{DDQ} = 1.2V (+/-0.06V) V _{pp} = +2.5V(-0.125V/+0.25V)
Operating Temperature	Commercial (0°C to 95°C)	Commercial (0°C to 95°C)	Commercial (0°C to 95°C)
Clock Frequency(MHz)	1600 (Max)	1333 (Max)	1333 (Max)
Data Rate (MT/s)	3200	2666	2666
CAS Latency	22	19	19
tRCD & tRP (ns)	13.75	14.25	14.25
Average Refresh Period	7.8uS at 0C ~ +85C 3.9uS at +85C to +95C	7.8uS at 0C ~ +85C 3.9uS at +85C to +95C	7.8uS at 0C ~ +85C 3.9uS at +85C to +95C
I/O Capacitance (Cio)	1.0pf	1.15pf	1.15pf
Pin to Pin Compatible	Pin to Pin Compatible		
I _{DD0} (mA)	101	93	92
I _{DD1} (mA)	109	101	142
I _{DD2P} (mA)	39	36	44
I _{DD2Q} (mA)	57	52	75
I _{DD2N} (mA)	61	55	74
I _{DD3P} (mA)	48	45	67
I _{DD3N} (mA)	98	88	86
I _{DD4R} (mA)	209	178	205
I _{DD4W} (mA)	242	208	230
I _{DD6N} (mA)	28	28	30
I _{DD6R} (mA)	25	25	25
I _{DD6A} (mA)	32	32	30
I _{DD7} (mA)	233	223	235
I _{DD5B} (mA)	187	180	180
Package 96b FBGA	7.5 x 13 x 1.2mm	7.5 x 13 x 1.2mm	7.5 x 13.5 x 1.2mm
FBGA Ball Dimensions	6.4 x 12mm (pitch 0.8mm)	6.4 x 12mm (pitch 0.8mm)	6.4 x 12mm (pitch 0.8mm)
Package Material	Pb and Halogen Free	Pb and Halogen Free	Pb and Halogen Free



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AS4C256M16D4A-62BIN / AS4C256M16D4A-75BIN vs AS4C256M16D4-75BIN Comparison

Part Number & result Parameter	AS4C256M16D4A-62BIN	AS4C256M16D4A-75BIN	AS4C256M16D4-75BIN
Product Description	DDR4 SDRAM, Rev.A	DDR4 SDRAM, Rev.A	DDR4 SDRAM
Capacity	4Gb (256M x 16)	4Gb (256M x 16)	4Gb (256M x 16)
Memory Organization	32Meg, x16bits, x4 banks x2 groups	32Meg, x16bits, x4 banks x2 groups	32Meg, x16bits, x4 banks x2 groups
Operating Power Supply	V _{DD} & V _{DDQ} = 1.2V (+/-0.06V) V _{pp} = +2.5V(-0.125V/+0.25V)	V _{DD} & V _{DDQ} = 1.2V (+/-0.06V) V _{pp} = +2.5V(-0.125V/+0.25V)	V _{DD} & V _{DDQ} = 1.2V (+/-0.06V) V _{pp} = +2.5V(0.125V/+0.25V)
Operating Temperature	Industrial (-40°C to 95°C)	Industrial (-40°C to 95°C)	Industrial (-40°C to 95°C)
Clock Frequency(MHz)	1600 (Max)	1333 (Max)	1333 (Max)
Data Rate (MT/s)	3200	2666	2666
CAS Latency	22	19	19
tRCD & tRP (ns)	13.75	14.25	14.25
Average Refresh Period	7.8uS at -40C ~ +85C 3.9uS at +85C to +95C	7.8uS at -40C ~ +85C 3.9uS at +85C to +95C	7.8uS at -40C ~ +85C 3.9uS at +85C to +95C
I/O Capacitance (C _{IO})	1.0pf	1.15pf	1.15pf
Pin to Pin Compatible	Pin to Pin Compatible		
I _{DD0} (mA)	101	93	92
I _{DD1} (mA)	109	101	142
I _{DD2P} (mA)	39	36	44
I _{DD2Q} (mA)	57	52	75
I _{DD2N} (mA)	61	55	74
I _{DD3P} (mA)	48	45	67
I _{DD3N} (mA)	98	88	86
I _{DD4R} (mA)	209	178	205
I _{DD4W} (mA)	242	208	230
I _{DD6N} (mA)	28	28	30
I _{DD6R} (mA)	25	25	25
I _{DD6A} (mA)	32	32	30
I _{DD7} (mA)	233	223	235
I _{DD5B} (mA)	187	180	180
Package 96b FBGA	7.5 x 13 x 1.2mm	7.5 x 13 x 1.2mm	7.5 x 13.5 x 1.2mm
FBGA Ball Dimensions	6.4 x 12mm (pitch 0.8mm)	6.4 x 12mm (pitch 0.8mm)	6.4 x 12mm (pitch 0.8mm)
Package Material	Pb and Halogen Free	Pb and Halogen Free	Pb and Halogen Free



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AS4C512M8D4A-75BCN vs AS4C512M8D4-75BCN Comparison

Parameter	AS4C512M8D4A-75BCN	AS4C512M8D4-75BCN
Product Description	DDR4 SDRAM Rev.A	DDR4 SDRAM Rev.0
Capacity	4Gb (512M x 8)	4Gb (512M x 8)
Memory Organization	32Meg, x8bits, x4 banks x4 groups	32Meg, x8bits, x4 banks x4 groups
Operating Power Supply	V _{DD} & V _{DDQ} = 1.2V (+/-0.06V) V _{pp} = +2.5V(-0.125V/+0.25V)	V _{DD} & V _{DDQ} = 1.2V (+/-0.06V) V _{pp} = +2.5V(-0.125V/+0.25V)
Operating Temperature	Commercial (0°C to 95°C)	Commercial (0°C to 95°C)
Clock Frequency(MHz)	1333	1333
Data Rate (MT/s)	2666	2666
CAS Latency	19	19
tRCD & tRP (ns)	14.25	14.25
Average Refresh Period	7.8uS at 0C ~ +85C 3.9uS at +85C to +95C	7.8uS at 0C ~ +85C 3.9uS at +85C to +95C
I/O Capacitance (Cio)	1.15pf	1.15pf
Pin to Pin Compatible	Pin to Pin Compatible	
AC/DC Characteristics	Same	Same
I_{DD0} (mA)	77	77
I_{DD1} (mA)	84	84
I_{DD2P} (mA)	36	36
I_{DD2Q} (mA)	52	52
I_{DD2N} (mA)	55	55
I_{DD3P} (mA)	45	45
I_{DD3N} (mA)	88	88
I_{DD4R} (mA)	128	128
I_{DD4W} (mA)	146	146
I_{DD6N} (mA)	28	28
I_{DD6R} (mA)	25	25
I_{DD6A} (mA)	32	32
I_{DD7} (mA)	179	179
Package 78b FBGA	7.5mm x 11mm x 1.2mm	7.5mm x 11mm x 1.2mm
Package Material	Pb and Halogen Free	Pb and Halogen Free



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AS4C512M8D4A-75BIN vs AS4C512M8D4-75BIN Comparison

Part Number & result Parameter	AS4C512M8D4A-75BIN	AS4C512M8D4-75BIN
Product Description	DDR4 SDRAM Rev.A	DDR4 SDRAM Rev.0
Capacity	4Gb (512M x 8)	4Gb (512M x 8)
Memory Organization	32Meg, x8bits, x4 banks x4 groups	32Meg, x8bits, x4 banks x4 groups
Operating Power Supply	V_{DD} & $V_{DDQ} = 1.2V (+/-0.06V)$ $V_{pp} = +2.5V(-0.125V/+0.25V)$	V_{DD} & $V_{DDQ} = 1.2V (+/-0.06V)$ $V_{pp} = +2.5V(-0.125V/+0.25V)$
Operating Temperature	Industrial (-40°C to 95°C)	Industrial (-40°C to 95°C)
Clock Frequency(MHz)	1333	1333
Data Rate (MT/s)	2666	2666
CAS Latency	19	19
tRCD & tRP (ns)	14.25	14.25
Average Refresh Period	7.8uS at -40C ~ +85C 3.9uS at +85C to +95C	7.8uS at -40C ~ +85C 3.9uS at +85C to +95C
I/O Capacitance (Cio)	1.15pf	1.15pf
Pin to Pin Compatible	Pin to Pin Compatible	
AC/DC Characteristics	Same	Same
I_{DD0} (mA)	77	77
I_{DD1} (mA)	84	84
I_{DD2P} (mA)	36	36
I_{DD2Q} (mA)	52	52
I_{DD2N} (mA)	55	55
I_{DD3P} (mA)	45	45
I_{DD3N} (mA)	88	88
I_{DD4R} (mA)	128	128
I_{DD4W} (mA)	146	146
I_{DD6N} (mA)	28	28
I_{DD6R} (mA)	25	25
I_{DD6A} (mA)	32	32
I_{DD7} (mA)	179	179
Package 78b FBGA	7.5mm x 11mm x 1.2mm	7.5mm x 11mm x 1.2mm
Package Material	Pb and Halogen Free	Pb and Halogen Free