



## Product Change Notification (PCN)

**PCN TRACKING NO: PCN# 20200721/SM**

**Date:** July 21, 2020

**Subject:** Product Change Notification (PCN) for Alliance 4G DDR3 'B' die options

<b>Description of Change:</b>	Product will only be offered in a new Die Revision – 'C'
<b>Reason for Change</b>	Product revision to provide continuous support to Alliance Memory's customers Part # change to new WAFER process technology
<b>Traceability, Guidelines (lot, date code, markings, shipment date)</b>	Traceable through marketing part number
<b>Updated Datasheet/s</b>	Part # has been changed and updated datasheets are posted on our website <a href="https://www.alliancememory.com/datasheets/AS4C256M16D3C/">https://www.alliancememory.com/datasheets/AS4C256M16D3C/</a> <a href="https://www.alliancememory.com/datasheets/AS4C256M16D3LC/">https://www.alliancememory.com/datasheets/AS4C256M16D3LC/</a>
<b>Summary of Changes between New and Old part numbers</b>	See <i>table 1</i> Below

**Table 1 - DDR3**

Density	Organisation	Alliance old Part Number	Alliance New Part Number
4G - B DIE	256M x 16	AS4C256M16D3B-12BCN	AS4C256M16D3C-12BCN
4G - B DIE	256M x 16	AS4C256M16D3B-12BCNTR	AS4C256M16D3C-12BCNTR
4G - B DIE	256M x 16	AS4C256M16D3B-12BIN	AS4C256M16D3C-12BIN
4G - B DIE	256M x 16	AS4C256M16D3B-12BINTR	AS4C256M16D3C-12BINTR
4G - B DIE	256M x 16	AS4C256M16D3B-12BAN	refer to 1.35V DDR3L option
4G - B DIE	256M x 16	AS4C256M16D3B-12BANTR	refer to 1.35V DDR3L option
4G - C DIE	256M x 16	New	AS4C256M16D3C-10BCN
4G - C DIE	256M x 16	New	AS4C256M16D3C-10BCNTR
4G - C DIE	256M x 16	New	AS4C256M16D3C-10BIN
4G - C DIE	256M x 16	New	AS4C256M16D3C-10BINTR
4G - C DIE	256M x 16	New	AS4C256M16D3C-93BCN
4G - C DIE	256M x 16	New	AS4C256M16D3C-93BCNTR
4G - B DIE	256M x 16	AS4C256M16D3LB-12BCN	AS4C256M16D3LC-12BCN
4G - B DIE	256M x 16	AS4C256M16D3LB-12BCNTR	AS4C256M16D3LC-12BCNTR
4G - B DIE	256M x 16	AS4C256M16D3LB-12BIN	AS4C256M16D3LC-12BIN



4G - B DIE	256M x 16	AS4C256M16D3LB-12BINTR	AS4C256M16D3LC-12BINTR
4G - B DIE	256M x 16	AS4C256M16D3LB-12BAN	AS4C256M16D3LC-12BAN
4G - B DIE	256M x 16	AS4C256M16D3LB-12BANTR	AS4C256M16D3LC-12BANTR
4G - B DIE	256M x 16	AS4C256M16D3LB-10BCN	AS4C256M16D3LC-10BCN
4G - B DIE	256M x 16	AS4C256M16D3LB-10BCNTR	AS4C256M16D3LC-10BCNTR
4G - B DIE	256M x 16	AS4C256M16D3LB-10BIN	AS4C256M16D3LC-10BIN
4G - B DIE	256M x 16	AS4C256M16D3LB-10BINTR	AS4C256M16D3LC-10BINTR
4G - B DIE	512M x 8	AS4C512M8D3LB-12BCN	AS4C512M8D3LC-12BCN
4G - B DIE	512M x 8	AS4C512M8D3LB-12BCNTR	AS4C512M8D3LC-12BCNTR
4G - B DIE	512M x 8	AS4C512M8D3LB-12BIN	AS4C512M8D3LC-12BIN
4G - B DIE	512M x 8	AS4C512M8D3LB-12BINTR	AS4C512M8D3LC-12BINTR
4G - B DIE	512M x 8	AS4C512M8D3LB-12BAN	AS4C512M8D3LC-12BAN
4G - B DIE	512M x 8	AS4C512M8D3LB-12BANTR	AS4C512M8D3LC-12BANTR
4G - B DIE	512M x 8	AS4C512M8D3LB-10BCN	x
4G - B DIE	512M x 8	AS4C512M8D3LB-10BCNTR	x
4G - B DIE	512M x 8	AS4C512M8D3LB-10BIN	x
4G - B DIE	512M x 8	AS4C512M8D3LB-10BINTR	x

**\* Any orders after January 31<sup>st</sup>, 2021 are Non-Cancelable/Non-Returnable and cannot be changed. Products cannot be returned in stock rotations after this date.**

Last Time Buy Date	January 31st, 2021
Last Time Ship Date	July 31st, 2021
Sample Available Date	Available Now
PCN Effective Date	July 21st, 2020



**Alliance Memory Inc.**

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Dear Valued Customer:

This letter provides End-of-Life (EOL) notice of products for 'B' die 30nm wafer technology for the 4G DDR3 (x16 FBGA). These products will move to new 25nm wafer technology 'C' die revision in Q3-2020.

The delivery deadline is **July 31st, 2021** with last time buy (LTB) deadline on **January 31st 2021**. Please note that the standard shipment dates will apply in general and extended delivery dates must be pre-arranged and accepted in writing by Alliance Memory Management.

Regarding the replacement, Alliance Memory has provided the new 25nm technology product line-up (refer to Table 1 above). The 'C' die products will provide better cost and performance. Samples are now available for customers to start verification procedures. We are also in full production of the 'C' die parts too.

Please see below comparison charts between the 'B' die vs. our new 'C' die.

Samples/Mass production are available now.

Please contact your local Alliance Memory representative if you have any questions regarding this information

Yours sincerely

  
David Bagby  
President  
Alliance Memory, Inc.

A circular blue stamp containing the Alliance Memory logo and the text: 'www.alliancememory.com • 12815 NE 124th Street STE#D Kirkland WA 98034 USA'.

## AS4C256M16D3B-12 & AS4C256M16D3C-12 Comparison

*(4Gb – 256M x 16, DDR3 rev.B vs rev.C Comparison)*

Part Number & result Parameter	<b>AS4C256M16D3B-12BCN AS4C256M16D3B-12BIN</b>	<b>AS4C256M16D3C-12BCN AS4C256M16D3C-12BIN</b>	<b>Comparison Result</b>
<b>Fab Process Technology</b>	<b>30nm</b>	<b>25nm</b>	Different
<b>Capacity</b>	4Gb (256M x 16)	4Gb (256M x 16)	Same
<b>Memory Organization</b>	32Mwords, x16bits, x8 banks	32Mwords, x16bits, x8 banks	Same
<b>Operating Power Supply</b>	$V_{DD} \& V_{DDQ} = 1.5 \pm 0.075V$ (1.425V to 1.575V)	$V_{DD} \& V_{DDQ} = 1.5 \pm 0.075V$ (1.425V to 1.575V)	Same
<b>Operating Temperature</b>	Commercial (0°C to 95°C) Industrial (-40 ~ 95°C)	Commercial (0°C to 95°C) Industrial (-40 ~ 95°C)	Same
<b>Clock Frequency</b>	800MHz	800MHz	Same
<b>CAS Latency</b>	11	11	Same
<b>tRCD &amp; tRP (ns)</b>	13.75	13.75	Same
<b>AC/DC Characteristics</b>	Same	Same	Same
<b>IDD Specification</b>			
<b>I<sub>DD0</sub> (mA)</b>	55	57	Comparable
<b>I<sub>DD1</sub> (mA)</b>	75	81	Comparable
<b>I<sub>DD2P0</sub> (mA)</b>	12	8	Comparable
<b>I<sub>DD2P1</sub> (mA)</b>	18	14	Comparable
<b>I<sub>DD2N</sub> (mA)</b>	25	24	Comparable
<b>I<sub>DD2Q</sub> (mA)</b>	25	24	Comparable
<b>I<sub>DD3P</sub> (mA)</b>	22	26	Comparable
<b>I<sub>DD3N</sub> (mA)</b>	32	38	Comparable
<b>I<sub>DD4R</sub> (mA)</b>	135	155	Comparable
<b>I<sub>DD4W</sub> (mA)</b>	150	155	Comparable
<b>I<sub>DD5B</sub> (mA)</b>	160	235	Comparable
<b>I<sub>DD6</sub> (mA)</b>	12	12	Same
<b>I<sub>DD6ET</sub> (mA)</b>	17	16	Comparable
<b>I<sub>DD7</sub> (mA)</b>	195	190	Comparable
<b>I<sub>DD8</sub> (mA)</b>	12	10	Comparable
<b>Package</b>	96-ball FBGA package (9 x 13.5 x 1.1mm)	96-ball FBGA package (7.5 x 13.5 x 1.2mm)	Ball matrix dimension same
<b>Package Material</b>	Pb and Halogen Free	Pb and Halogen Free	Same
<b>Reliability &amp; QA</b>	Same	Same	Same



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## AS4C256M16D3LB-10 & AS4C256M16D3LC-10 Comparison

*(4Gb – 256M x 16, DDR3L rev.B vs rev.C Comparison)*

Part Number & result Parameter	<b>AS4C256M16D3LB-10BCN AS4C256M16D3LB-10BIN</b>	<b>AS4C256M16D3LC-10BCN AS4C256M16D3LC-10BIN</b>	<b>Comparison Result</b>
<b>Fab Process Technology</b>	<b>30nm</b>	<b>25nm</b>	Different
<b>Capacity</b>	4Gb (256M x 16)	4Gb (256M x 16)	Same
<b>Memory Organization</b>	32Mwords, x16bits, x8 banks	32Mwords, x16bits, x8 banks	Same
<b>Operating Power Supply</b>	$V_{DD}$ & $V_{DDQ} = 1.35V$ (1.283V to 1.45V)	$V_{DD}$ & $V_{DDQ} = 1.35V$ (1.283V to 1.45V)	Same
<b>DDR3 Compatibility</b>	Compatible to $1.5 \pm 0.075V$	Compatible to $1.5 \pm 0.075V$	Same
<b>Operating Temperature</b>	Commercial (0°C to 95°C) Industrial (-40 ~ 95°C)	Commercial (0°C to 95°C) Industrial (-40 ~ 95°C)	Same
<b>Clock Frequency</b>	933MHz	933MHz	Same
<b>CAS Latency</b>	13	13	Same
<b>tRCD &amp; tRP (ns)</b>	13.91	13.91	Same
<b>AC/DC Characteristics</b>	Same	Same	Same
<b>IDD Specification</b>			
<b>I<sub>DD0</sub> (mA)</b>	60	59	Comparable
<b>I<sub>DD1</sub> (mA)</b>	80	84	Comparable
<b>I<sub>DD2P0</sub> (mA)</b>	12	8	RevC better
<b>I<sub>DD2P1</sub> (mA)</b>	19	16	RevC better
<b>I<sub>DD2N</sub> (mA)</b>	25	26	Comparable
<b>I<sub>DD2Q</sub> (mA)</b>	25	26	Comparable
<b>I<sub>DD3P</sub> (mA)</b>	22	28	Comparable
<b>I<sub>DD3N</sub> (mA)</b>	34	40	Comparable
<b>I<sub>DD4R</sub> (mA)</b>	150	165	Comparable
<b>I<sub>DD4W</sub> (mA)</b>	165	165	Same
<b>I<sub>DD5B</sub> (mA)</b>	160	242	Comparable
<b>I<sub>DD6</sub> (mA)</b>	12	12	Same
<b>I<sub>DD6ET</sub> (mA)</b>	17	16	Comparable
<b>I<sub>DD7</sub> (mA)</b>	215	200	RevC better
<b>I<sub>DD8</sub> (mA)</b>	12	10	Comparable
<b>Package</b>	96-ball FBGA package (9 x 13.5 x 1.1mm)	96-ball FBGA package (7.5 x 13.5 x 1.2mm)	Ball matrix dimension same
<b>Package Material</b>	Pb and Halogen Free	Pb and Halogen Free	Same
<b>Reliability &amp; QA</b>	Same	Same	Same



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## AS4C256M16D3LB-12 & AS4C256M16D3LC-12 Comparison

*(4Gb – 256M x 16, DDR3L rev.B vs rev.C Comparison)*

Part Number & result Parameter	<b>AS4C256M16D3LB-12BCN</b> <b>AS4C256M16D3LB-12BIN</b>	<b>AS4C256M16D3LC-12BCN</b> <b>AS4C256M16D3LC-12BIN</b>	<b>Comparison Result</b>
<b>Fab Process Technology</b>	<b>30nm</b>	<b>25nm</b>	Different
<b>Capacity</b>	4Gb (256M x 16)	4Gb (256M x 16)	Same
<b>Memory Organization</b>	32Mwords, x16bits, x8 banks	32Mwords, x16bits, x8 banks	Same
<b>Operating Power Supply</b>	$V_{DD} \& V_{DDQ} = 1.35V$ (1.283V to 1.45V)	$V_{DD} \& V_{DDQ} = 1.35V$ (1.283V to 1.45V)	Same
<b>DDR3 Compatibility</b>	Compatible to $1.5 \pm 0.075V$	Compatible to $1.5 \pm 0.075V$	Same
<b>Operating Temperature</b>	Commercial (0°C to 95°C) Industrial (-40 ~ 95°C)	Commercial (0°C to 95°C) Industrial (-40 ~ 95°C)	Same
<b>Clock Frequency</b>	800MHz	800MHz	Same
<b>CAS Latency</b>	11	11	Same
<b>tRCD &amp; tRP (ns)</b>	13.75	13.75	Same
<b>AC/DC Characteristics</b>	Same	Same	Same
<b>IDD Specification</b>			
<b>I<sub>DD0</sub> (mA)</b>	55	57	Comparable
<b>I<sub>DD1</sub> (mA)</b>	75	81	Comparable
<b>I<sub>DD2P0</sub> (mA)</b>	12	8	RevC better
<b>I<sub>DD2P1</sub> (mA)</b>	18	14	RevC better
<b>I<sub>DD2N</sub> (mA)</b>	25	24	Comparable
<b>I<sub>DD2NT</sub> (mA)</b>	30	30	Comparable
<b>I<sub>DD2Q</sub> (mA)</b>	25	24	Comparable
<b>I<sub>DD3P</sub> (mA)</b>	22	26	Comparable
<b>I<sub>DD3N</sub> (mA)</b>	32	38	Comparable
<b>I<sub>DD4R</sub> (mA)</b>	135	155	Comparable
<b>I<sub>DD4W</sub> (mA)</b>	150	155	Comparable
<b>I<sub>DD5B</sub> (mA)</b>	160	235	Comparable
<b>I<sub>DD6</sub> (mA)</b>	12	12	Comparable
<b>I<sub>DD6ET</sub> (mA)</b>	17	16	Comparable
<b>I<sub>DD7</sub> (mA)</b>	195	190	RevC better
<b>I<sub>DD8</sub> (mA)</b>	12	10	Comparable
<b>Package</b>	96-ball FBGA package (9 x 13.5 x 1.1mm)	96-ball FBGA package (7.5 x 13.5 x 1.2mm)	Ball matrix dimension same
<b>Package Material</b>	Pb and Halogen Free	Pb and Halogen Free	Same
<b>Reliability &amp; QA</b>	Same	Same	Same

## AS4C512M8D3LB-12 & AS4C512M8D3LC-12 Comparison

*(4Gb – 512M x 8, DDR3L rev.B vs rev.C Comparison)*

Part Number & result Parameter	AS4C512M8D3LB-12BCN AS4C512M8D3LB-12BIN	AS4C512M8D3LC-12BCN AS4C512M8D3LC-12BIN	Comparison Result
<b>Fab Process Technology</b>	<b>30nm</b>	<b>25nm</b>	Different
<b>Capacity</b>	4Gb (512M x 8)	4Gb (512M x 8)	Same
<b>Memory Organization</b>	64Mwords, x8bits, x8 banks	64Mwords, x8bits, x8 banks	Same
<b>Operating Power Supply</b>	$V_{DD} \& V_{DDQ} = 1.35V$ (1.283V to 1.45V)	$V_{DD} \& V_{DDQ} = 1.35V$ (1.283V to 1.45V)	Same
<b>DDR3 Compatibility</b>	Compatible to $1.5 \pm 0.075V$	Compatible to $1.5 \pm 0.075V$	Same
<b>Operating Temperature</b>	Commercial (0°C to 95°C) Industrial (-40 ~ 95°C)	Commercial (0°C to 95°C) Industrial (-40 ~ 95°C)	Same
<b>Clock Frequency</b>	800MHz	800MHz	Same
<b>CAS Latency</b>	11	11	Same
<b>tRCD &amp; tRP (ns)</b>	13.75	13.75	Same
<b>AC/DC Characteristics</b>	Same	Same	Same
<b>IDD Specification</b>			
<b>I<sub>DD0</sub> (mA)</b>	45	47	Comparable
<b>I<sub>DD1</sub> (mA)</b>	60	61	Comparable
<b>I<sub>DD2P0</sub> (mA)</b>	10	8	Comparable
<b>I<sub>DD2P1</sub> (mA)</b>	16	14	Comparable
<b>I<sub>DD2N</sub> (mA)</b>	20	24	Comparable
<b>I<sub>DD2Q</sub> (mA)</b>	25	24	Comparable
<b>I<sub>DD3P</sub> (mA)</b>	15	26	Comparable
<b>I<sub>DD3N</sub> (mA)</b>	30	30	Same
<b>I<sub>DD4R</sub> (mA)</b>	85	95	Comparable
<b>I<sub>DD4W</sub> (mA)</b>	90	95	Comparable
<b>I<sub>DD5B</sub> (mA)</b>	160	235	Comparable
<b>I<sub>DD6</sub> (mA)</b>	10	12	Comparable
<b>I<sub>DD6ET</sub> (mA)</b>	15	16	Comparable
<b>I<sub>DD7</sub> (mA)</b>	150	130	RevC better
<b>I<sub>DD8</sub> (mA)</b>	10	10	Same
<b>Package</b>	78-ball FBGA package (9 x 10.6 x 1.1mm)	78-ball FBGA package (7.5 x 10.6 x 1.2mm)	Ball matrix dimension same
<b>Package Material</b>	Pb and Halogen Free	Pb and Halogen Free	Same
<b>Reliability &amp; QA</b>	Same	Same	Same